



EL979950083

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/388,857
Filing Date September 1, 1999
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner L.M. Schillinger
Attorney's Docket No. MI22-878
Title: Semiconductor Processing Methods Of Forming Transistors, Semiconductor
Processing Methods Of Forming Dynamic Random Access Memory Circuitry, And
Related Integrated Circuitry

RESPONSE TO AUGUST 5, 2003 OFFICE ACTION

To: Mail Stop Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: D. Brent Kenady
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DEC 17 2003
TECHNOLOGY CENTER 2800

Responsive to the Office Action dated August 5, 2003, Applicant amends
and remarks as follows:

AMENDMENTS

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.

12/10/2003 EAREGAY1 00000038 09388857

02 FC:1202

36.00 OP

In th Drawings

Please modify Fig. 3 as shown in the Annotated, marked-up-in-red, copy of Fig. 3 enclosed herewith. A revised formal drawing labeled Replacement Sheet is also enclosed for the Examiner's approval.